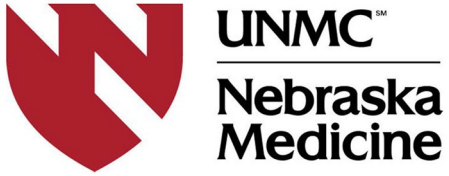
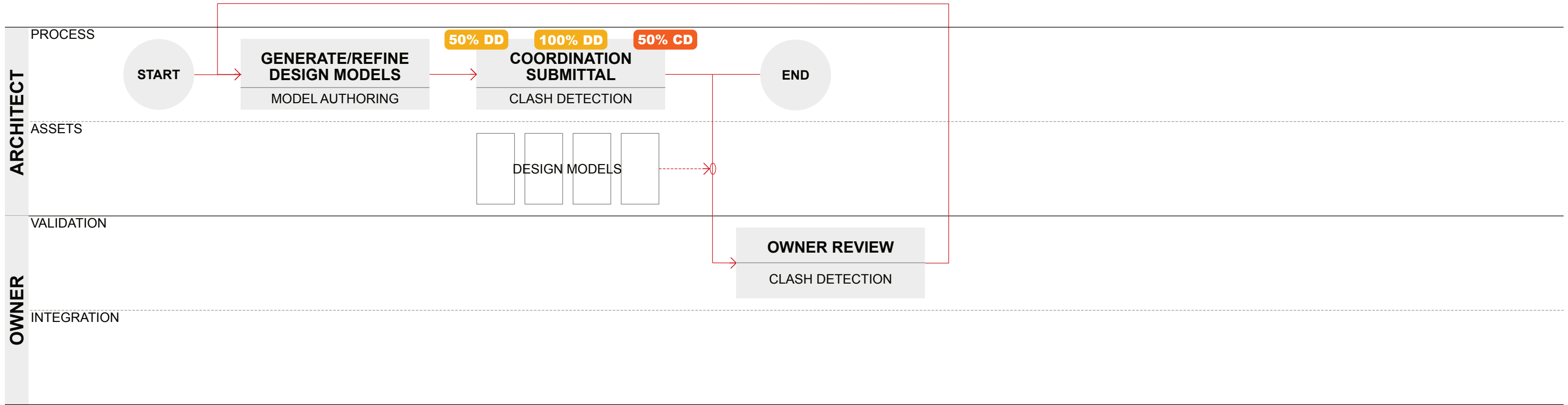


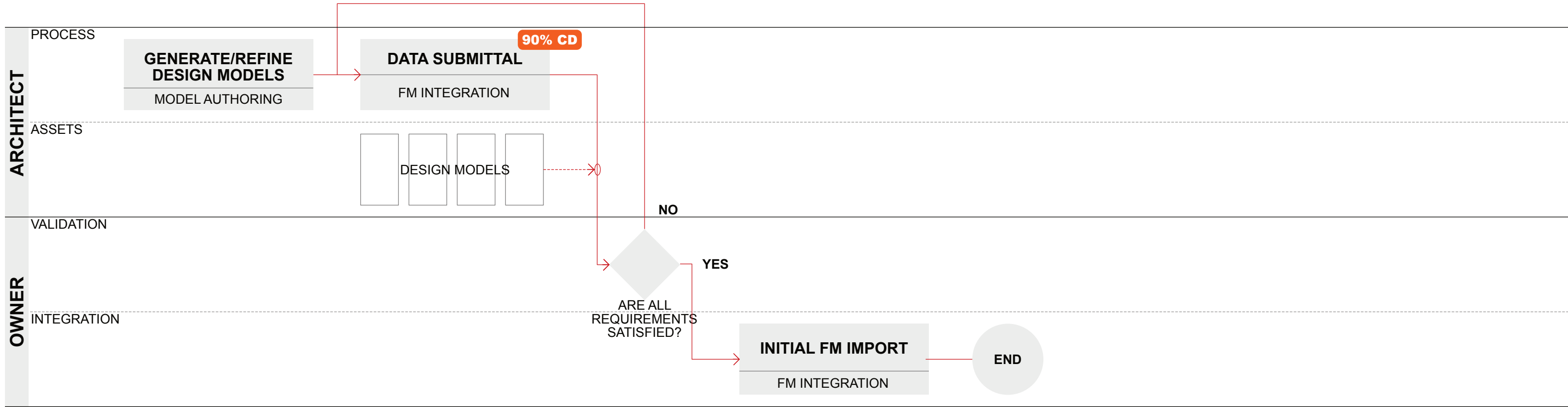
SPATIAL ANALYSIS (PROGRAMMING) – PROCESS DIAGRAM 1





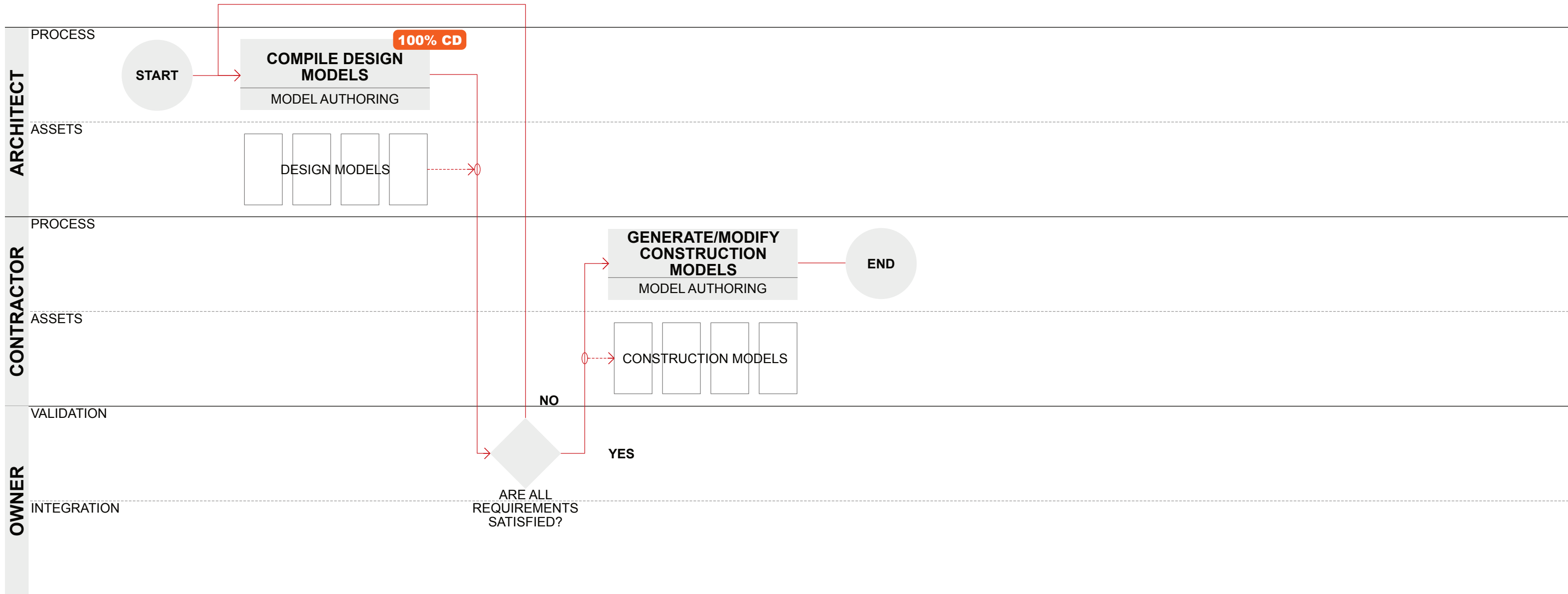
CLASH DETECTION (DESIGN) – PROCESS DIAGRAM 2



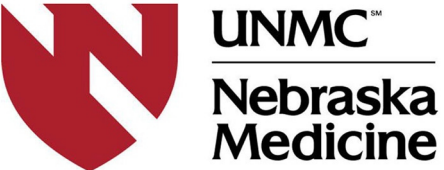


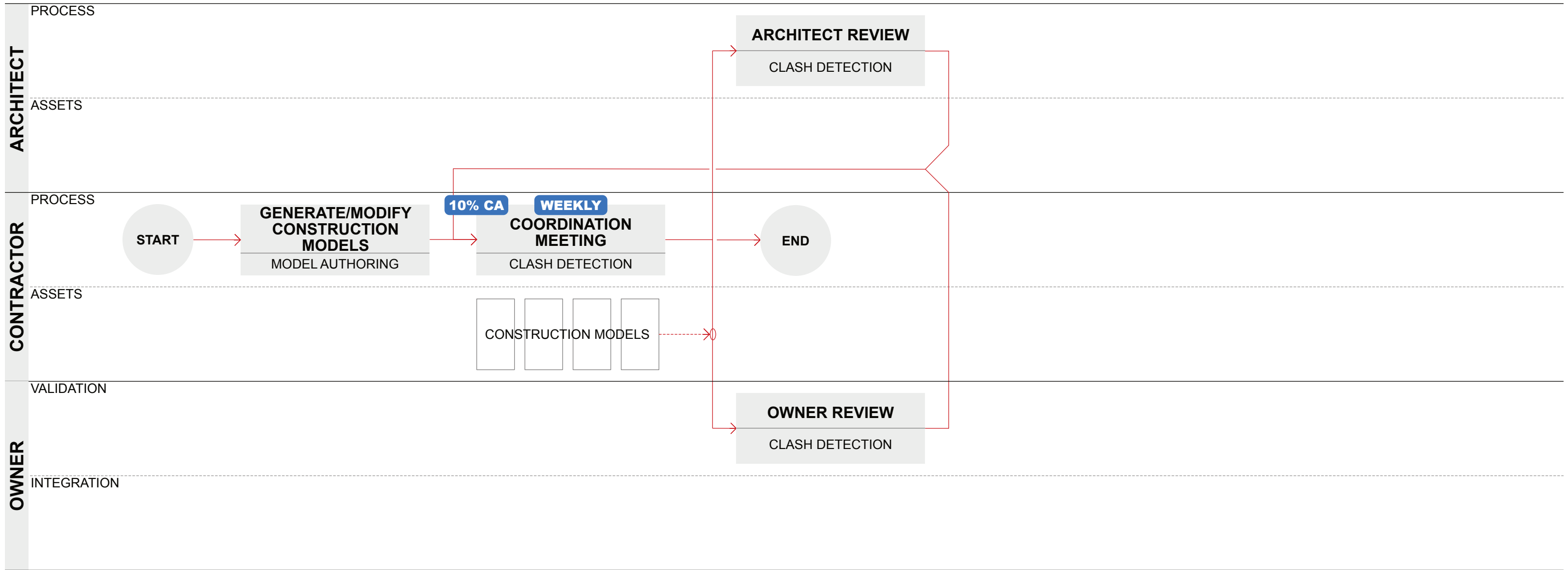
FM INTEGRATION (DESIGN) – PROCESS DIAGRAM 3





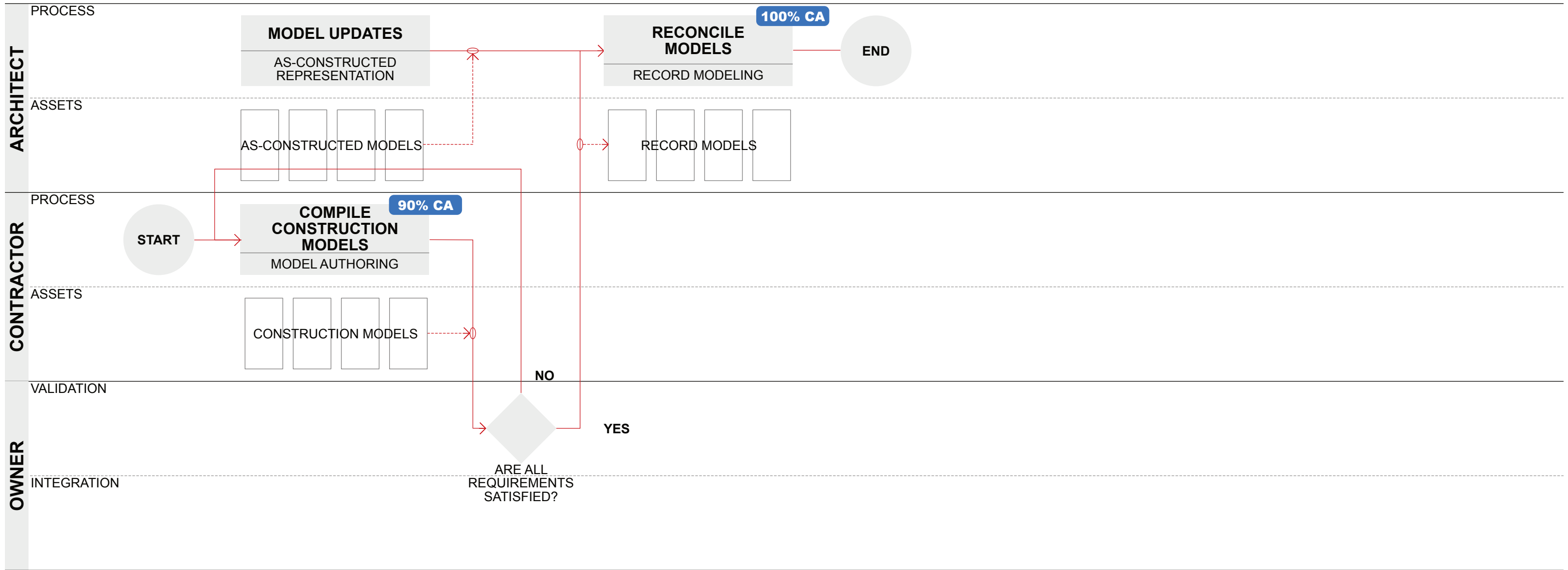
MODEL HANDOFF (DESIGN) – PROCESS DIAGRAM 4





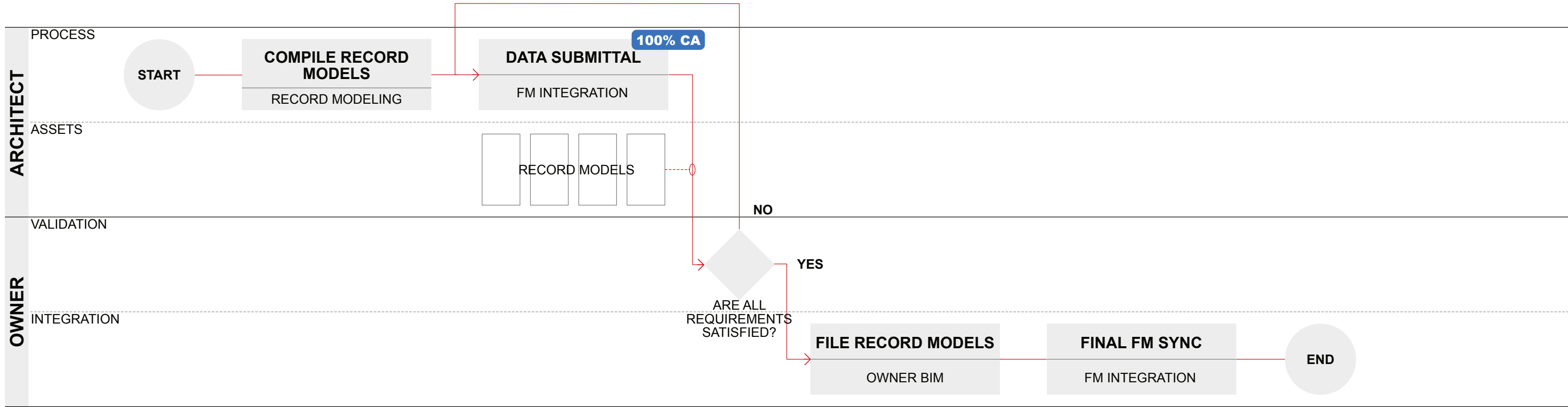
CLASH DETECTION (CONSTRUCTION) – PROCESS DIAGRAM 5





MODEL HANDOFF (CONSTRUCTION) – PROCESS DIAGRAM 6





FM INTEGRATION (CLOSEOUT) – PROCESS DIAGRAM 7

